



Attorney's Docket No.: 10559-607001
Intel Docket No.: P12891

AF

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : William R. Wheeler, et al.
Serial No. : 10/054,179
Filed : January 17, 2002
Title : MODELING A LOGIC DESIGN

Art Unit : 2825
Examiner : Annette M. Thompson
Assignee : Intel Corporation

Entered w/ RCE

dt'd. 3/9/04

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DNK
3/24/06

AMENDMENT IN REPLY TO ACTION OF OCTOBER 6, 2005

Please amend the above-identified application as follows:

Do Not Enter
6-7-2006

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

January 6, 2006
Date of Deposit

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